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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.
09/280,541	03/30/99	HO	J 755657

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EXAMINER

NGUYEN, K

ART UNIT

PAPER NUMBER

2674

DATE MAILED:

10/12/00

Please find below and/or attached an Office communication concerning this application or proceeding.

Commissioner of Patents and Trademarks

Office Action Summary

Application No.

09/280,541

Applicant(s)

HO, JAE-ICK

Examiner

Kevin M. Nguyen

Art Unit

2674

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136 (a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-11 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-11 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claims ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are objected to by the Examiner.
- 11) ☐ The proposed drawing correction filed on ____ is: a) ☐ approved b) ☐ disapproved.
- 12) ☐ The oath or declaration is objected to by the Examiner.

Priority under 35 U.S.C. § 119

- 13) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d).
- a) ☐ All b) ☐ Some * c) ☐ None of the CERTIFIED copies of the priority documents have been:
1. ☐ received.
2. ☐ received in Application No. (Series Code / Serial Number) ____.
3. ☐ received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

- 14) ☐ Acknowledgement is made of a claim for domestic priority under 35 U.S.C. & 119(e).

Attachment(s)

- 15) ☒ Notice of References Cited (PTO-892) 18) ☐ Interview Summary (PTO-413) Paper No(s). ____
- 16) ☒ Notice of Draftsperson's Patent Drawing Review (PTO-948) 19) ☐ Notice of Informal Patent Application (PTO-152)
- 17) ☐ Information Disclosure Statement(s) (PTO-1449) Paper No(s) ____ 20) ☐ Other: ____

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(e) the invention was described in a patent granted on an application for patent by another filed in the United States before the invention thereof by the applicant for patent, or on an international application by another who has fulfilled the requirements of paragraphs (1), (2), and (4) of section 371(c) of this title before the invention thereof by the applicant for patent.

2. Claims 1-11 are rejected under 35 U.S.C. 102(e) as being anticipated by Clement (US Patent No. 5,726,668).

As to claims 1-3 and 6, Clement teaches a manufacturing process control system 62 and display system 42, 44, 46 which includes any of a variety of data input devices may be utilized (column 5, lines 29-20). Further, Clement teaches :

“The process variable data may consist of digital inputs (DIs), digital outputs (DOs), analog inputs (AIs), or analog outputs (AOs) utilized by the process control system 62 and interconnected through one or more data I/O interfaces 70, 72, 74 to the manufacturing process equipment and instruments on the plant floor (column 5, lines 8-13).

Referring to FIG. 4, each of the GPDPs repeatedly run a series of interrupt routines in addition to the main routine. FIG. 6 illustrates the interrupt controller and the main interrupt routines implemented by the system of the present invention. At each interrupt, the system checks each of UART Channel A (the primary communication path), UART Channel B (the secondary communication

path), and the board-to-board UART to see if there are any incoming messages (column 11, lines 16-24).”

3. As to claim 4, Clement teaches a programmable logic device (PLD) 184 is programmed to server as the address decoder and logic decoder for the two channel (column 15, lines 4-6).

4. As to claim 5, Clement teaches the processor board 82, 84 (column 14, lines 66-67) which includes a LED 1 and LED 2 (column 15, line 14).

5. As to claim 7-9 and 11, Clement teaches

FIG. 13 illustrates the alarm bulb routine. The system checks, at 178, to determine whether a particular alarm variable is set (i.e., as the alarm has been activated) (column 14, lines 19-21).

FIG. 4 illustrates the basic logic employed by the Graphic Processor Boards 82 and 84 at start-up of the system 40. At start up, memory is cleared and the main routine and interrupt routines are initiated.

As shown in FIG. 5, the main routine first determines, at 110, whether it is time to update the LCD display 75 on the keypad 76. If it is time (preferably every one second), the system updates the LCD display with one of a series of recurring diagnostic messages. In the preferred embodiment, the display alternately shows the operator various diagnostic messages, including such information as the panel number, the system version number, the last system revision date, and which processor (82 or 84) is transmitting the data to the bulbs (column 8, lines 15-28).

6. As to claim 10, Clement teaches a switch 284 is provided as a board select switch for

setting a unique decoder board number for each board (column 16, lines 35-36).

Conclusion

7. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. See PTO- 892 form.

8. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Kevin M. Nguyen whose telephone number is 703-305-6209. The examiner can normally be reached on Monday through Thursday 8 am-5pm..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Richard Hjerpe can be reached on 703-305-4709. The fax phone numbers for the organization where this application or proceeding is assigned are 703-308-6606 for regular communications and 703-308-6606 for After Final communications.

Any inquiry of a general nature or relating to the status of this application or proceeding should be directed to the receptionist whose telephone number is 703-305-3900.

Kevin M. Nguyen
September 27, 2000



RICHARD A. HJERPE
SUPERVISORY PATENT EXAMINER
GROUP 2700